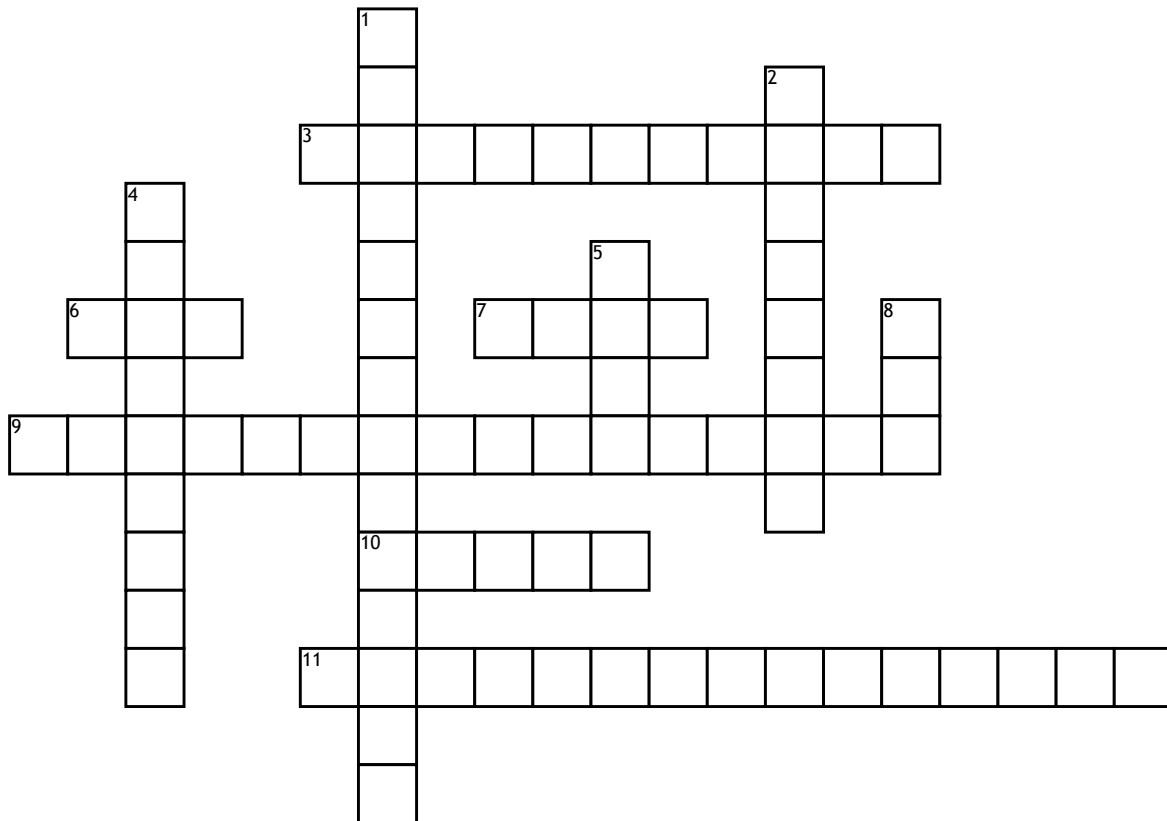


Name: _____

SSD unit 1



Across

3. The period of translating a user's source program is called..
6. Expand program status word
7. L 2,904(4,1) performs what type of instructions.
9. A hardware that transfers data between the MBR and the core memory location the address of which is in the MAR
10.is a pseudo code the indicates to the assembler which general purpose register to use as a base and what its contents will be
11. refers to data stored in core memory

Down

1. Location counter is also known as....
2. mechanisms where by the assembler creates data areas for the programmer, containing constants based on requests.
4. Which converts high level to low level language?
5.is machine opcode that load a register with the next address and branch to the address in the second field. Since second operand is 0 so the control will go to the next instruction.
8.contains the address of the memory location that is to be read from or stored into