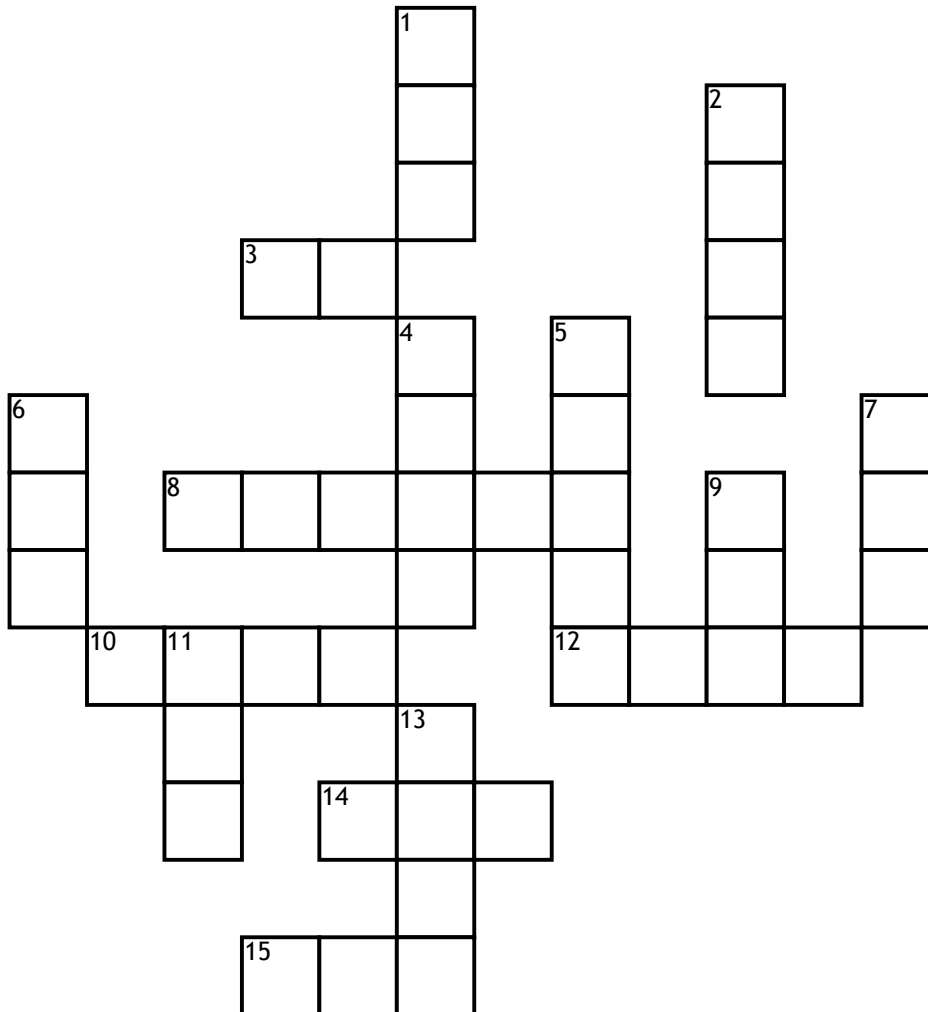


Micro puzzle



Across

3. Which register is used as a 16-bit i/o address pointer
8. Which flag are used to record specific characteristics of arithmetic and logical instructions:
10. The 8086 fetches the instruction one after another from which segment of the memory
12. Status register is also called
14. Which instruction that is used to convert the result of addition of two packed bcd number to a valid bcd number

15. In 8086 microprocessor which signal is used to interface the odd bank memory

Down

1. Which logical instruction is used to negate the last four bits of a data without effecting the first four bit
2. The instruction that performs logical AND operation and the result of the operation is not available is
4. Which bus is bidirectional
5. Which instruction that pushes flag register onto the stack

6. Which interrupt has the highest priority in among the two major interrupts?

7. The instruction that unconditionally transfers the control of execution to the specified address is

9. instruction that loads effective address formed by destination operand into the specified source register is

11. In 8255 which Stroked signal is used in which mode

13. In 8254 the counter starts counting only if which signal is high