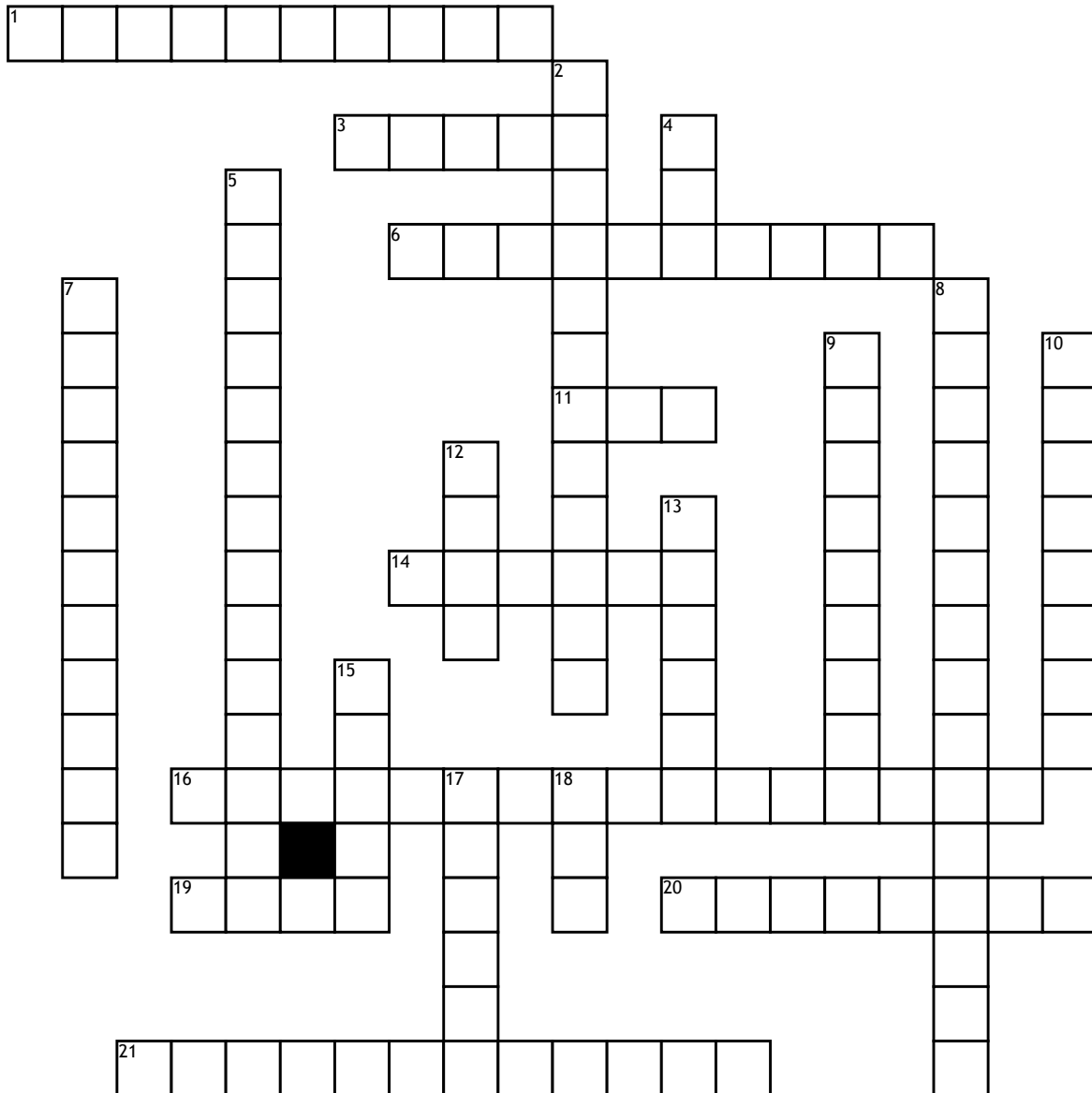


Name: \_\_\_\_\_

# Machine Architecture



## Across

1. Equivalent to the width of the data bus
3. Used to connect I/O devices to the system
6. Machine architecture with a single CPU
11. Also known as Main Memory
14. CPU asserts this to identify stability of buses
16. Equivalent to the  $2^{\text{of}}$  of the width of the address bus
19. Used to create Cache

20. A bus transfers data in this way

21. Not isolated I/O

## Down

2. Serial is associated with...
4. Component responsible for processing
5. Component used to enable which chips are required for the current operation
7. Parallel data transfer is associated with...
8. Synchronizes components through pulses

9. Identifies which addresses are assigned to which process

10. Fastest unit of memory

12. Only bus which is bi-directional over the same lines

13. Data transfer used to resolve skewing

15. Type of ROM which requires UV light for erasure

17. All three buses together

18. If you are going to perform an AND function this is the unit you will need