

Name: \_\_\_\_\_

Date: \_\_\_\_\_

# Logic

J L O D X U D T I Z K Y Q L L E U B P G G Z M K  
E G A T L O V B M Y S A A W V W C H G K A M X I  
O C O C Q Z Y R E Q L T R I R Q B O N O F T J W  
L O G G N F A O M N I P T N C G M Q R E S Z E M  
I U K H A D A X A G V A P P A Z K E A O H U K S  
W T A M G T S L I U G E X U E U S E J M R N M X  
A P L F R H F D S E J G R P S I G J P T I G L S  
A U D Y O E S Q N E C B L W S Q C H X L R O M P  
N T U Z M C V D U Y O H F T Q X M E T O G J B Y  
N N M Y E I S I N N N Q O K O F Q U U I H E E E  
O P G J D I P N T G T R R H S Q P N C C E B O L  
H O J M X M A V B S R F O E T N D P T X V S K B  
Y W X N I N M L G C O S T Q I B R I L T C M R V  
E E L E I T O R X C L P A F D O W L B I B E F B  
L R A O T Y L R S P N B C V B S B C L U A K I T  
B S T A G Y U N B A O O I E L F Y L M D F N P S  
A U R S U I U F N P L O D L P B I G B N A F Q V  
T P T E E H C D S M W L N J A S N O U R L R E G  
H P I R E T R E V N I E I U C C A R Y W I O A R  
T L J D N Q D G R Q F A D O D R S A L G G N K L  
U Y A O X Q A L D Z W N P R D F P N K Z Y X P I  
R C Z F W A U C E S P E B X O F H D S E H I L K  
T H D F G R E F E R N C E M R Y U F B K K B C L  
G F K N Z T I U C R I C D E T A R G R E T N I F

intergrated circuit  
breadboard  
negative  
digital  
ground  
gates  
nand  
off

oscillscope  
indicator  
refernce  
postive  
output  
input  
true  
xor

logic probe  
demorgan  
resistor  
voltage  
supply  
logic  
xnor  
on

powersupply  
inverter  
boolean  
binary  
switch  
link  
and  
or

truth table  
karnaugh  
control  
buffer  
false  
maps  
nor