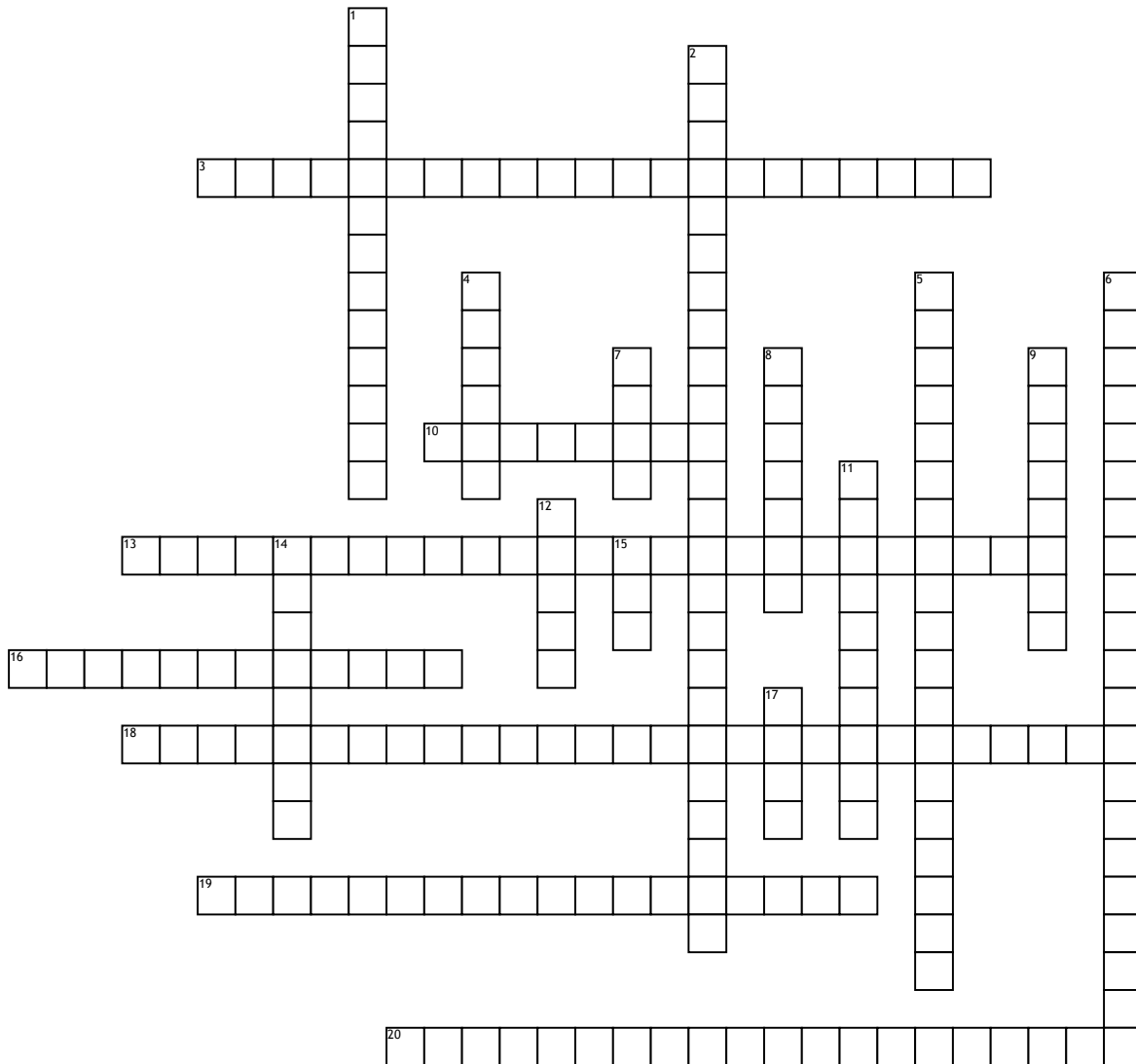


Name: _____

Date: _____

LIC1



Across

- 3. CIRCUIT THAT AMPLIFIES DIFFERENCE BETWEEN TWO INPUT SIGNALS
- 10. WHICH IS SAID TO BE PASSIVE LOAD?
- 13. CIRCUIT IN INPUT STAGE OF OPAMP
- 16. MAKING DC VOLTAGE ZERO
- 18. INTERMEDIATE STAGE
- 19. AMPLIFIER THAT AMPLIFIES INVERTED SIGNAL
- 20. DIRECT COUPLED AMPLIFIER?

Down

- 1. OUTPUT CURRENT IS THE MIRROR IMAGE OF INPUT
- 2. RATIO OF DIFFERENTIAL MODE AND COMMON MODE GAIN
- 4. EMITTER FOLLOWER
- 5. CIRCUIT THAT HAS EMITTER RESISTANCE TO INCREASE CMRR
- 6. CIRCUIT THAT PRODUCES AMPLIFIED SIGNAL WITH ZERO PHASE SHIFT
- 7. RATIO OF COLLECTOR CURRENT TO BASE CURRENT?

8. SEMICONDUCTOR SUITABLE FOR FABRICATION OF IC?

- 9. RATE OF CHANGE OF OUTPUT TO INPUT?
- 11. CIRCUIT WHICH HAS CURRENT MIRROR AS LOAD?
- 12. RATIO OF EMITTER CURRENT TO BASE CURRENT
- 14. GAIN IN IDEAL OPAMP
- 15. TRANSISTORS USED AS LOAD IN ACTIVE LOAD OF DIFFERENTIAL AMPLIFIER
- 17. OUTPUT IMPEDENCE IN IDEAL OPERATIONAL AMPLIFIER