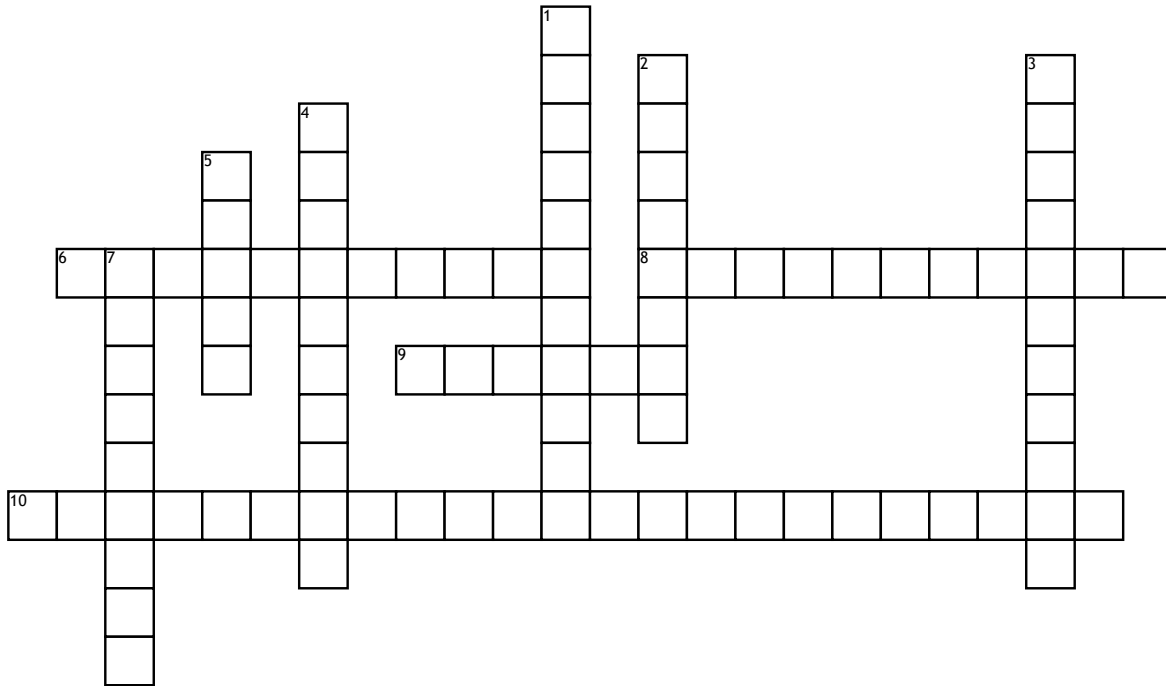


Combinational and Sequential Circuit



Across

6. A combinational circuit that has n inputs and one output is called as

8. A type of counter that uses clock pulse

9. A $4 * 3$ multiplier requires _____ number of AND gates

10. A combinational circuit that avoids propagation delay

Down

1. Edge triggered Flip flop that uses two Latches is called as

2. Group of Flip flops makes a

3. _____ delay occurs in Ripple Adder

4. Logic circuit used to compare two binary numbers

5. Type of Storage element that changes its state in the level of the clock pulse

7. A Shift register that performs all kind of shifts and parallel load