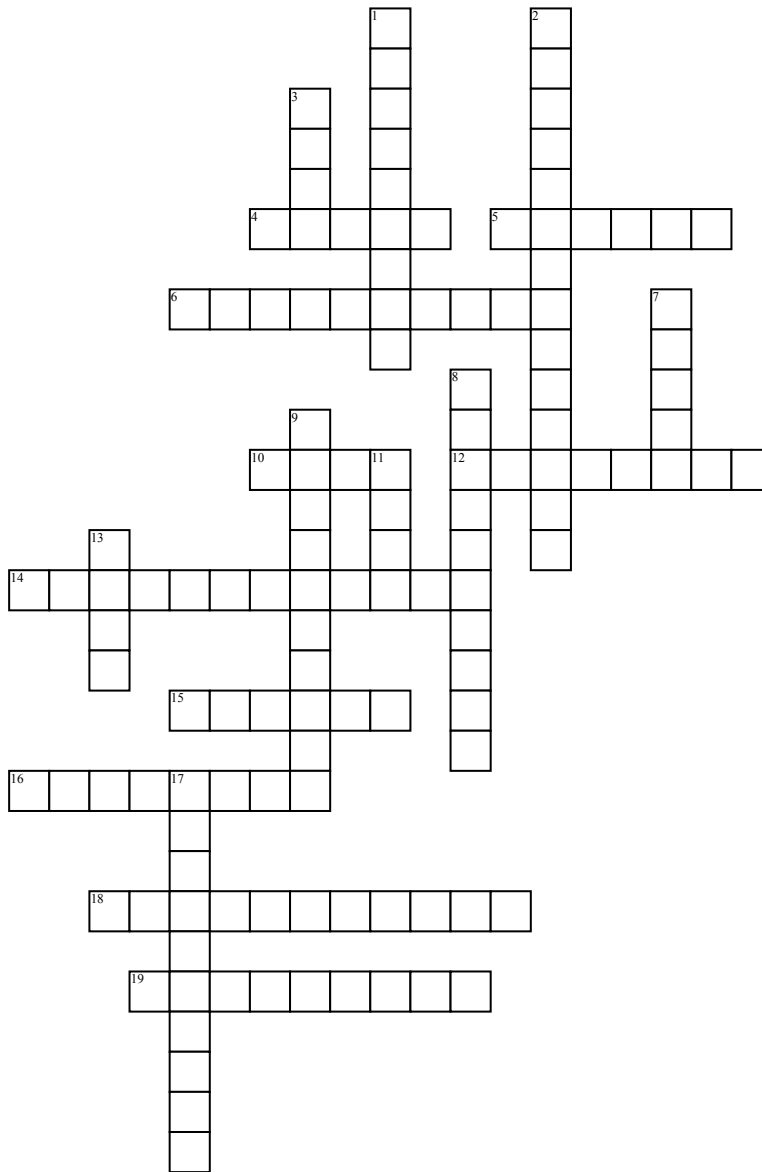


ADVANCED MICROCONTROLLER CROSSWORD



Across

4. By default, INT0-INT2 interrupts are _____ triggered.

5. _____ bits control the period of the clock.

6. Baud rate is the reverse of the _____.

10. Which register is responsible for handling all the external hardware interrupts?

12. _____ is the sampling clock in the oversampling mode.

14. The process of reduction of a continuous input to a discrete output is _____.

15. _____ flag gets cleared if the interrupt is serviced.

16. protection from failures to the system is the main function of _____ timer.

18. Here the word sigma represents.

19. The number of repeated closeness to the true value is accounted by _____.

Down

1. According to conventions being followed, R12 to R15 are used for _____ passing.

2. A microcontroller can be considered a self-contained system with a processor, memory and peripherals and can be used as an _____.

3. Most of the MSP430's devices have _____ number of timers in it.

7. _____ register used for masking the interrupts flags of the Timer0, Timer1 and Timer2.

8. The SPDT switch can be used as a _____ circuit.

9. It is the technique through which a processor can communicate to its associate partners when an appropriate hardware is not available for its purpose. This communication is made possible through the software.

11. MSP430 describes _____ instruction as Format1 addressing.

13. _____ instruction can act as valid BCD addition.

17. To ensure that a negative fluctuation does not trigger an unwanted downward transition by pulling the input down through VIT⁻, we must choose minimum.